

HIGH LEVEL SYNTHESIS

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[High-level synthesis - Wikipedia](#)

High-level synthesis (HLS), sometimes referred to as C synthesis, electronic system-level (ESL) synthesis, algorithmic synthesis, or behavioral synthesis, is an automated design process that interprets an algorithmic description of a desired behavior and creates digital hardware that implements that behavior.

[High-Level Synthesis - Xilinx | DigiKey](#)

The Xilinx Vivado High-Level Synthesis (HLS) tool transforms a C specification into a register transfer level (RTL) implementation that you can synthesize into a Xilinx field programmable gate array (FPGA).

[High-Level Synthesis - Quora](#)

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Kidney International, Vol. 35 (1989), pp. 817823.

Neutralization of the anionic sites of cultured rat mesangialcells by poly-L-lysine. FRANCESCO

PUGLIESE, PAOLO MENE, MARIA CRISTINA

ANANIA, and GILLO A. CINOTTI

[Intel High Level Synthesis Compiler: Getting Started Guide](#)

The Intel High Level Synthesis Compiler Getting Started Guide provides instructions to help get you up and running. Read this guide for instructions on downloading and installing the Intel HLS Compiler, and review the tutorials and design examples listed in the guide to learn about the features and functions of the Intel HLS Compiler.

[High-Level Synthesis \(HLS\) theory](#)

Description of what High-Level Synthesis is and its main steps.

[How High-Level Synthesis Can Raise the Efficiency of ...](#)

The high-level synthesis tool used for the purpose of this study is Catapult C Synthesis [9]. The quality of results achieved with this tool [10] is a mandatory requirement in the event of deploying the proposed approach in a production environment. In this research we are looking at

reusing an IP within changing technology, interfaces and micro-architectural specifications. The aim of this